## ctr|X - CORE

- Communication with XM
  - Connection using Modbus



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In the next tutorial we will see how a communication works using the Modbus protocol to pass data from a ctrlX to an XM and vice versa.

The system uses the Server - Client model



The Modbus operating modules that we are going to use allow the passage of up to four types of areas.

I / O Area 65536 Bit Coil 65536 Discrete Input 65536 Word Input Register

65536 Word Holding Register

<u>(1)</u>

Although this is relatively true, the reality is that the allocation of areas in both teams, differs slightly in the part called Coil.

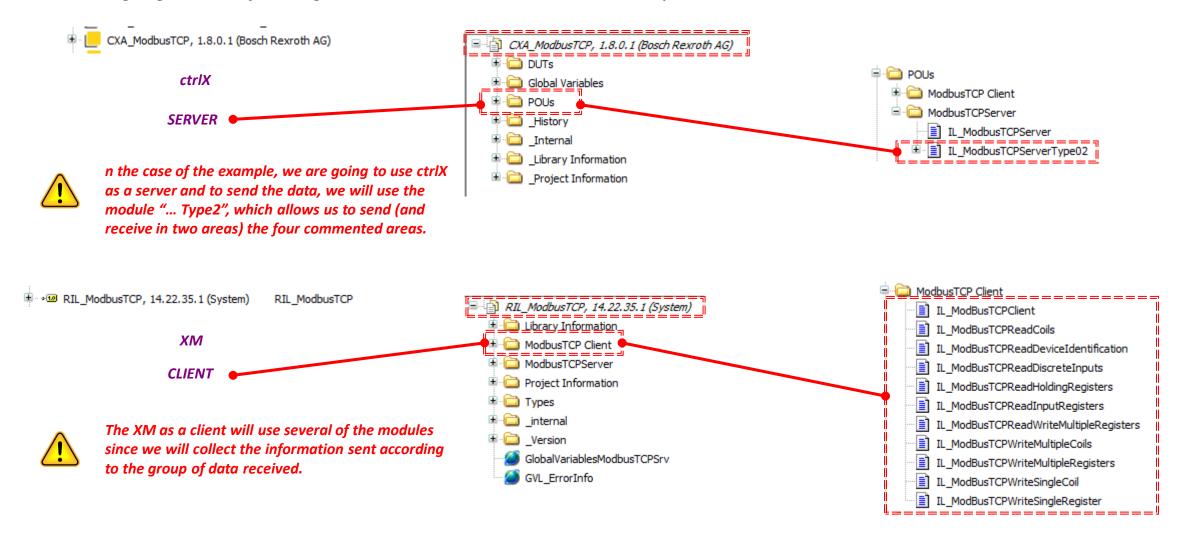
So care must be taken when transferring data.

I / O Area	ctrlX	
65536 Bit Coil	Byte	•
65536 Discrete Input	Byte 📗 🗕	•
65536 Word Input Register	Word	
65536 Word Holding Register	Word	
I / O Area	XM	
i, SAICU	/\/\VI	
65536 Bit Coil	Bool	•
		•
65536 Bit Coil	Bool	•



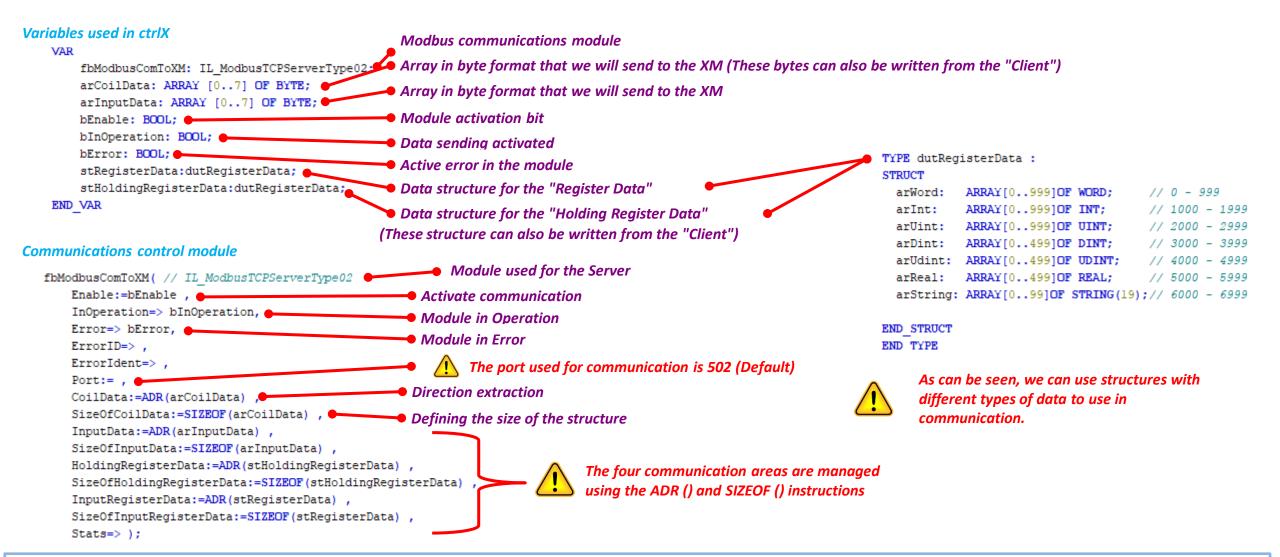
#### ctrlX - Libraries used for Modbus communication

#### For this we are going to use the following libraries that must be installed on both computers





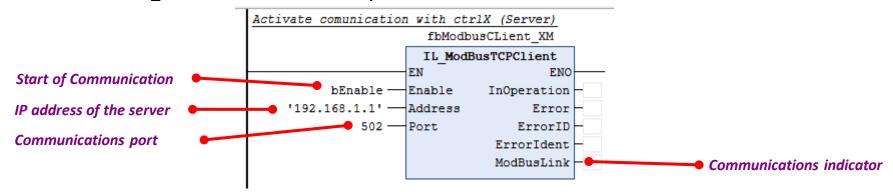
#### Module used in ctrlX to send data. This module also allows us to read the CoilData and HoldingRegisterData that can be modified from the Client



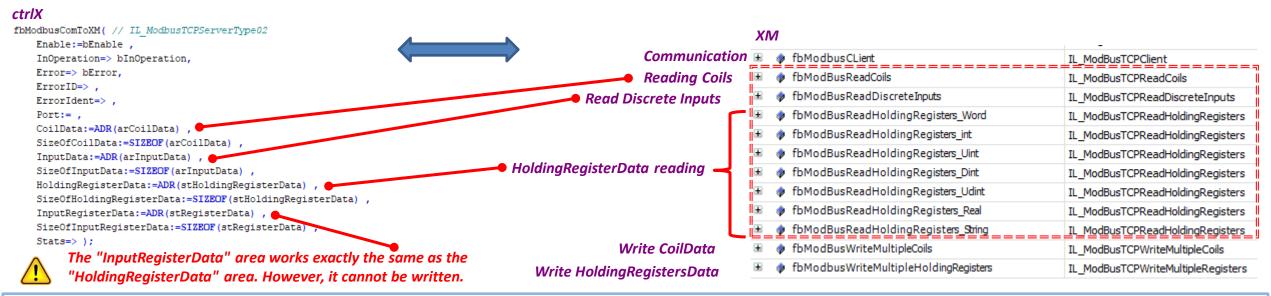


#### ctr|X - Communications modules in XM

As we have commented previously, on the client side, more modules are used to carry out the readings or, where appropriate, the writings. The first module to use will be the "IL\_ModbusTCPClient" that helps us to establish communication and obtain the communications indicator.



The ctrlX communications module transmits the data to the XM, in some cases as read / write and must be "read" or "written according to the provisions of the generated structures themselves, as described below.





#### The data sent or received as we have said can be of four different types:

COIL DATA:	Coil Data		
	Options :	Read / Write	
	Structure :	Byte (ctrlX) / Bool (XM)	
	Define array size with :	SIZEOF()	
	Representation :	1 Array / 8 bits	
	Optional :	Use of structures	

arCoilData: ARRAY [0..7] OF BYTE; arCoilData: ARRAY [0..7] OF BOOL; XM

Input Data					
Options : Read					
Structure :	Byte (ctrlX) / Bool (XM)				
Define array size with :	SIZEOF()				
Representation :	1 Array / 8 bits				
Optional :	Use of structures				

arInputData: ARRAY [0...7] OF BYTE; arInputData: ARRAY [0..7] OF BOOL;

HoldingRegisterData		HOLDING REGISTER DATA:	
Options :	Read / Write		
Structures :	Word		
Define array size with :	SIZEOF()		
Representation :	Word (16 bits)		TYPE dutRegisterData :
Optional :	Use of structures		STRUCT

stHoldingRegisterData:dutRegisterData;

**REGISTER DATA:** RegisterData **Optiones:** Read Structures: Word Define array size with: SIZEOF() Word (16 bits) **Representation:** Optional: Use of structures // 0 - 999

stRegisterData:dutRegisterData;

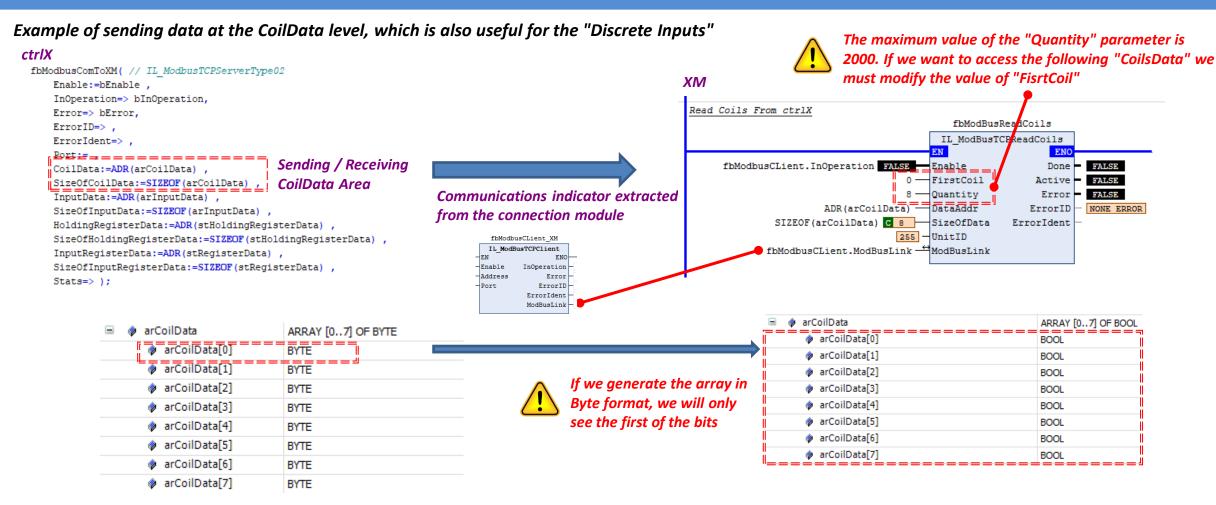
arInt: ARRAY[0..999]OF INT; // 1000 - 1999 arUint: ARRAY[0..999]OF UINT; // 2000 - 2999 ARRAY[0..499]OF DINT; arDint: // 3000 - 3999 arUdint: ARRAY[0..499]OF UDINT; // 4000 - 4999 ARRAY[0..499]OF REAL; arReal: // 5000 - 5999 arString: ARRAY[0..99]OF STRING(19);// 6000 - 6999

**INPUT DATA:** 

END STRUCT END TYPE

arWord:

ARRAY[0..999]OF WORD;

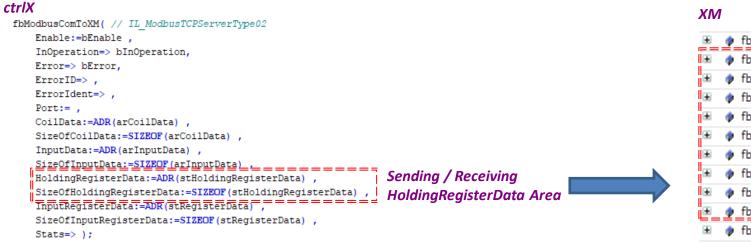




As already mentioned, both areas are different in the two teams. ctrlX sends the data in byte format so the first transmit byte sends or receives the first seven bits of the XM area



#### Example of sending data at the HoldingRegisterData level, which also serves for the RegisterData



ARRAY [0..499] OF REAL

ARRAY [0..99] OF STRING(19)

~~~		. <del>-</del>
Ŧ	fbModbusCLient	IL_ModBusTCPClient
$\pm$	fbModBusReadCoils	IL_ModBusTCPReadCoils
Ŧ	fbModBusReadDiscreteInputs	IL_ModBusTCPReadDiscreteInputs
+	fbModBusReadHoldingRegisters_Word	$IL\_ModBusTCPReadHoldingRegisters$
Ŧ	fbModBusReadHoldingRegisters_int	$IL\_ModBusTCPReadHoldingRegisters$
<b>±</b>	fbModBusReadHoldingRegisters_Uint	$IL\_ModBusTCPReadHoldingRegisters$
Ŧ	fbModBusReadHoldingRegisters_Dint	$IL\_ModBusTCPReadHoldingRegisters$
<b>±</b>	fbModBusReadHoldingRegisters_Udint	$IL\_ModBusTCPReadHoldingRegisters$
+	fbModBusReadHoldingRegisters_Real	$IL\_ModBusTCPReadHoldingRegisters$
<b>±</b>	fbModBusReadHoldingRegisters_String	IL ModBusTCPReadHoldingRegisters
<b>±</b>	fbModBusWriteMultipleCoils	IL_ModBusTCPWriteMultipleCoils
<b>±</b>	fbModbusWriteMultipleHoldingRegisters	IL_ModBusTCPWriteMultipleRegisters

fbModBusReadHoldingRegisters\_Word

The data must be extracted in individual groups, if we want to access the different parts of the generated structure, therefore the same type of module is used, but each one for a different group.

<b>=</b>	stHoldingRegisterData	dutRegisterData
<b>±</b>	arWord	ARRAY [0999] OF WORD
<b>±</b>	<pre>arInt</pre>	ARRAY [0999] OF INT
<b>±</b>	arUint	ARRAY [0999] OF UINT
<b>±</b>	arDint	ARRAY [0499] OF DINT
±	arUdint	ARRAY [0499] OF UDINT



**XM** 



ctrlX

arReal

arString

#### ctrlX - Example of control of HoldingRegisterData - Use of structures to send complex data

The communication structure of the HoldingRegisters area is in Word format, so we must calculate at which point each read / write sector is initialized.



The first 4 areas of the structure are in Word format, so the actual occupation corresponds 1 to 1. The following areas are in double word format, so the assigned value \* 2 should be multiplied, in this way we can adjust the values and knowing what area we can work on.

From the example and with the structure shown, each area has a starting point

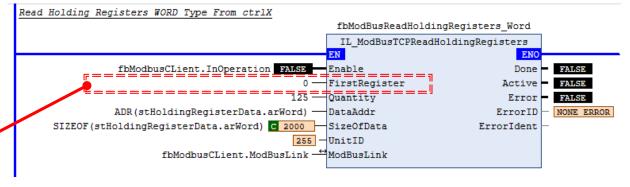
stHoldingRegisterData	dutRegisterData	
	ARRAY [0999] OF WORD	0
⊞ 🧳 arInt	ARRAY [0999] OF INT	1000
🛨 🧳 arUint	ARRAY [0999] OF UINT	2000
🛨 🧳 arDint	ARRAY [0499] OF DINT	3000
	ARRAY [0499] OF UDINT	<u>4000</u>
⊞ 📦 arReal	ARRAY [0499] OF REAL	5000
	ARRAY [099] OF STRING(19)	6000

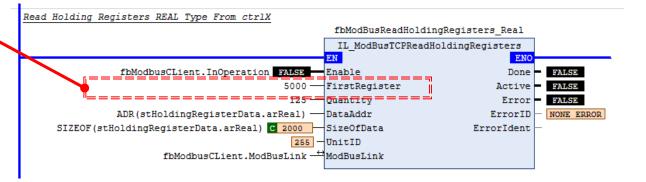
The modules only allow to send 125 elements at a time by means of the parameter "Quantity", if we have an area of 1000 words, for example, we must take into account that access should be done in eight blocks, modifying in this case the value assigned in "

FirstRegister "

Start End

	Start	End
1	0	124
2	125	249
3	250	374
4	375	499
5	500	624
6	625	749
7	750	874
8	875	999







#### ctrlX - Modules for data extraction used in the example

#### Modules used in the example in reading only the first 125 variables of each group



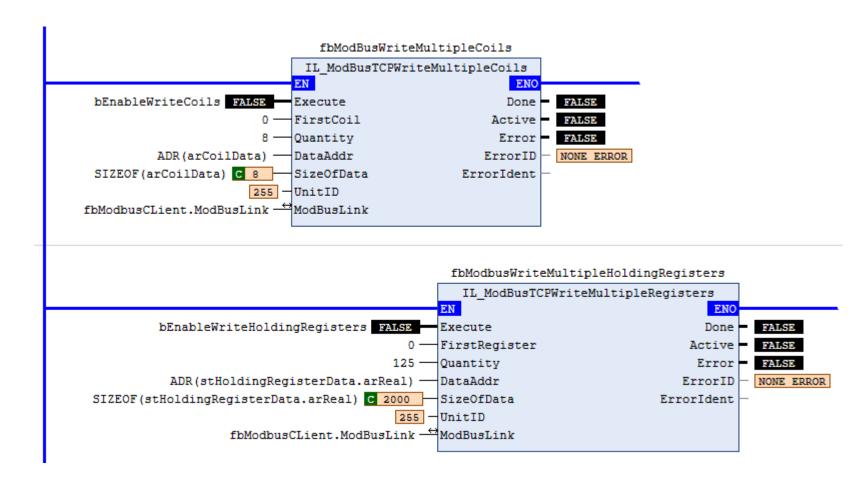
#### ctrlX - Modules for writing CoilsData and HoldingRegisterData

The writing modules for multiple Coils or multiple registers allow us to modify the CoilsData and HoldingRegisterData values from the client side.



The operating structure for writing data is exactly the same as in the case of reading and we must modify the parameters "FirstRegister" and "Quantity" to be able to access the different areas

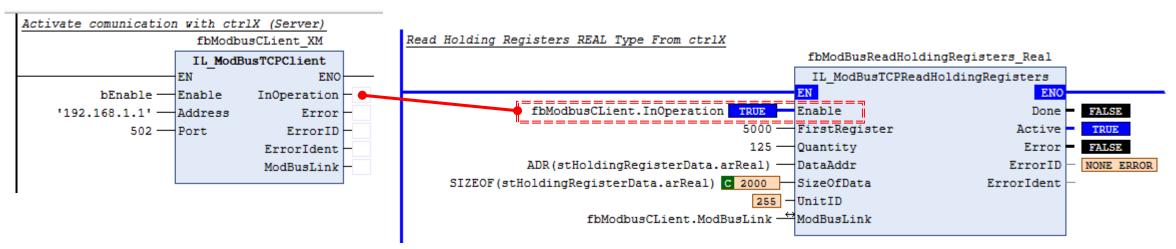
_	-11	-   -   -   -   -   -   -   -   -   -	1.5 5 .
	Sth	loldingRegisterData	dutRegisterData
+	•	arWord	ARRAY [0999] OF WORD
+	•	arInt	ARRAY [0999] OF INT
$\pm$	•	arUint	ARRAY [0999] OF UINT
$\pm$	•	arDint	ARRAY [0499] OF DINT
$\pm$	•	arUdint	ARRAY [0499] OF UDINT
$\pm$	•	arReal	ARRAY [0499] OF REAL
±	•	arString	ARRAY [099] OF STRING(19)





### ctrlX - Bit "InOperation" + Notes

In the previous examples the read modules are activated directly with the "InOperation" bit. However, it must be in mind that if we want to write data, this signal should be deactivated because if it does not continue writing the value sent by the Server

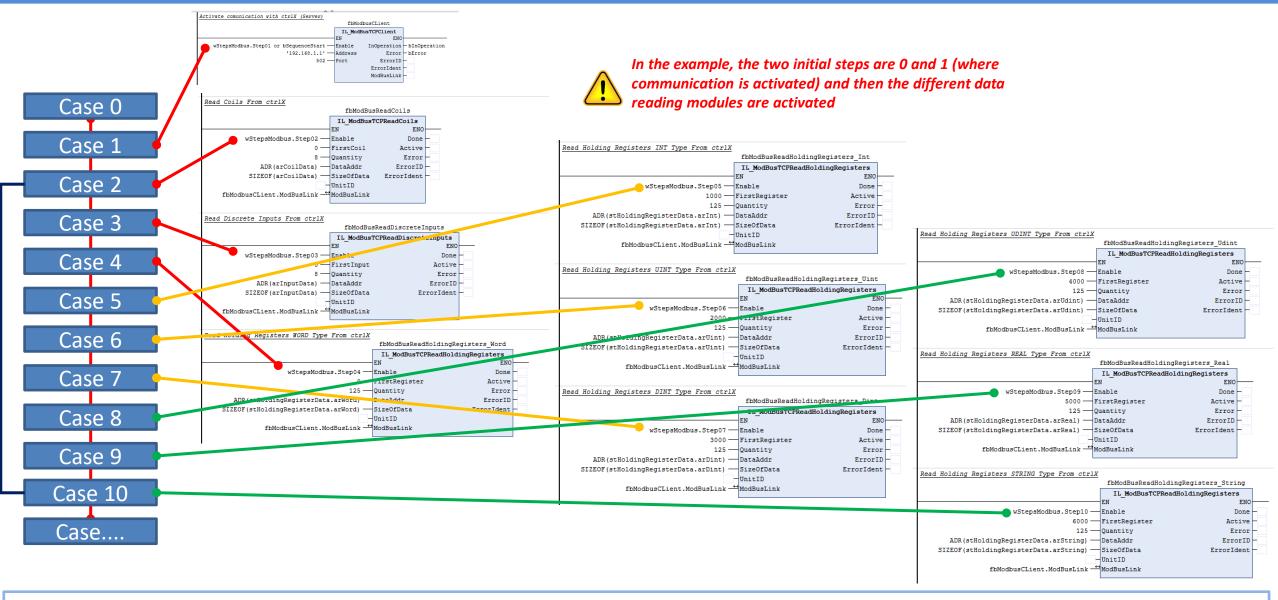


#### **Notes:**

- It is recommended to use a sequence of steps to perform a controlled sending of the data.
- Failure to do this may cause errors
- The communication must be associated to a standard Task and never to a Sercos task.
- The sequence used in the XM part manages the control of the reads and the error in the initial communication.
- This programming is done my way and it is obvious that the end goal can be achieved in many ways.
- This is just a small example of Modbus communication and some program changes may be necessary to get the best results.
- As we have some areas for reading and writing and others for reading only, we can use them separately and in this way manage the data sent and the data received.
- In some modules the error F\_Related\_Table may appear, additional 1: "1817", additional 2: "13", this error does not appear in the lists but it can be generated by a too high value in "Quantity"

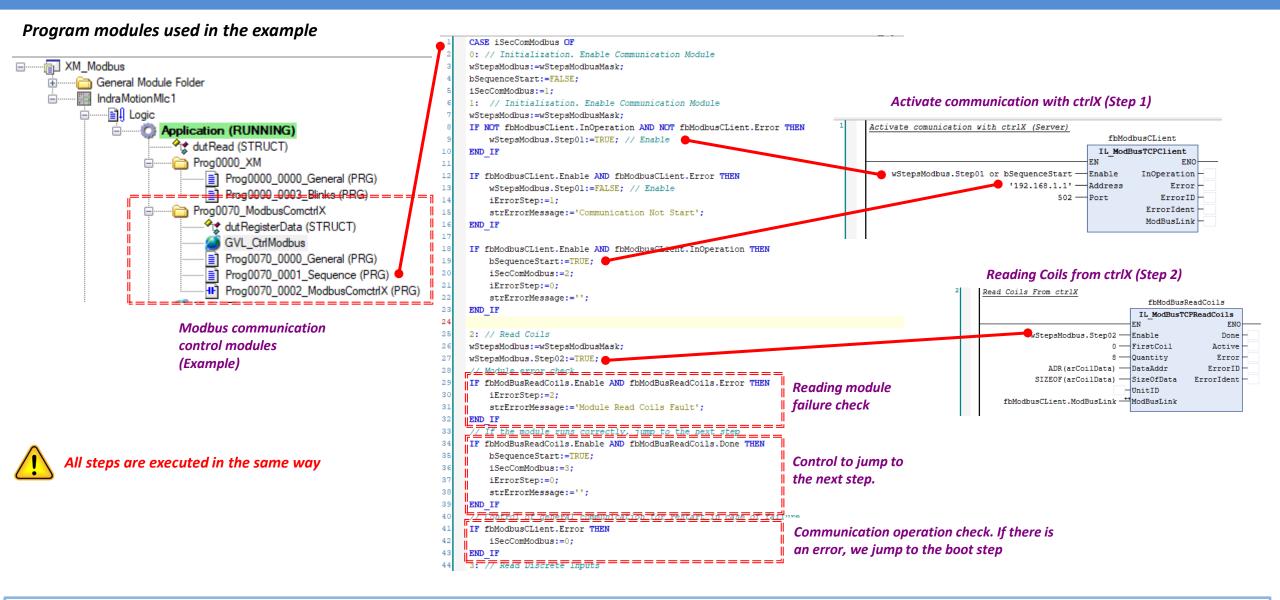


#### ctrlX - Example with Case sequence (With only reading modules)





#### ctrlX - Example with Case sequence (With only reading modules)





Graph of the Enable states. As can be seen there is "something" strange, since the fourth bit, corresponding to the values of type "Int" is practically always



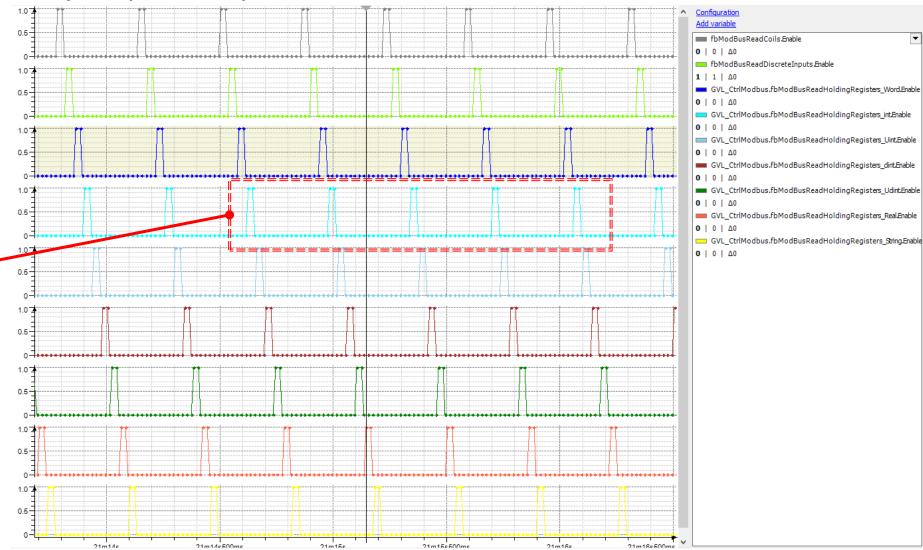
the enable of the "Int" type variables module. This even generates a "Delay" in the

reception of signals.





#### In the following graph you can see the operation of the sequence correctly

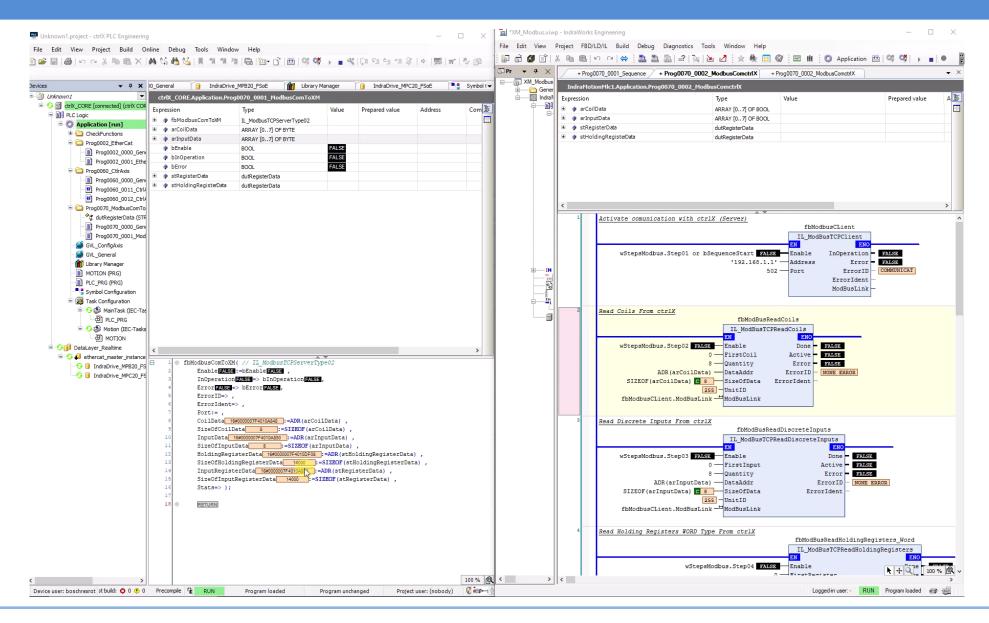




Fixed the sequence control, the steps are executed correctly

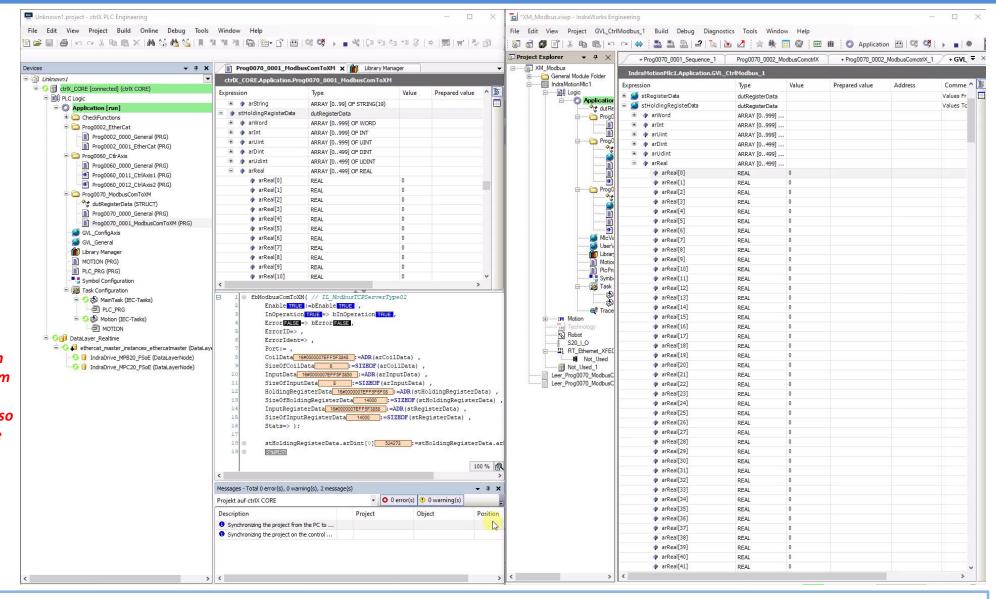


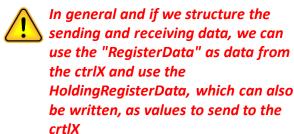
#### ctrlX - Video example with Case sequence (Send Values to XM)





#### ctrlX - Video example with Case sequence (Send Values to ctrlX)







# Thanks you for your attention



